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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/809,925	03/25/2004	Hiroshi Yamazaki	1324.70190	6761
7590 Patrick G. Burns, Esq. GREER, BURNS & CRAIN, LTD. Suite 2500 300 South Wacker Drive Chicago, IL 60606			EXAMINER OSORIO, RICARDO	
			ART UNIT 2629	PAPER NUMBER
			MAIL DATE 10/07/2010	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/809,925

Applicant(s)

YAMAZAKI, HIROSHI

Examiner

RICARDO L. OSORIO

Art Unit

2629

Period for Reply -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 July 2010.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-9 and 13 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-6 is/are allowed.
- 6) ☒ Claim(s) 7-9 and 13 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SG-08)
- 4) ☐ Interview Summary (PTO-413)
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____
- Paper No(s)/Mail Date _____

DETAILED ACTION

1. The indicated allowability of claims 7-9 is withdrawn in view of the newly discovered reference(s) to Fujino et al. (US 2003/0043126). Rejections based on the newly cited reference(s) follow.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

3. Claims 7-9 and 13 are rejected under 35 U.S.C. 102(a) as being anticipated by Fujino et al. (US 2003/0043126).

As to claim 7, Fujino discloses a liquid crystal display device comprising: a liquid crystal display panel (Fig. 4, character (ch.) 4); a plurality of data driver ICs for driving data lines of the liquid crystal display panel (Fig. 4, ch. 1); a first clock signal line for transmitting a first clock signal to the plurality of data driver ICs (Fig. 1, DDF 12A, and paragraphs 71 and 74); a second clock signal line which is equipped in parallel with the first clock signal line and transmits a second clock signal which is in reverse relation with the first clock signal Fig. 1, DDF 12B, and paragraphs 71 and 74); and a timing controller for outputting the first and second clock signals to the first and second clock signal lines respectively (Fig. 7, ch. 600, and paragraph 13); wherein each of the data driver ICs input the first and second clock signals and a selection signal, select the first or second clock signal based on the selection signal, and can selectively latch data signals with the first or second clock signal (paragraphs 71, 74 and 116).

As to claim 8, Fujino discloses a data signal of a dot is consisted of a plurality of bits (paragraph 71), and the data driver IC has input pins for data signals arranged so that the data signal of an odd-number dot of each bit of each color and the data signal of an even-number dot of the same bit are adjacent to each other (paragraphs 71 and 74)

As to claim 9, Fujino discloses a data driver IC for a liquid crystal display device comprising: a first data latch that inputs a first clock signal and latches data signals of odd-number dots with the first clock signal (Fig. 1, DDF 12A and paragraphs 71 and 74); a second data latch that inputs a second clock signal in reverse relation with the first clock signal and latches data signals of even-number dots with the second clock signal (Fig. 1, DDF 12B, and paragraphs 71 and 74); and a sampling memory (Fig. 5, ch. 14) that samples and stores said data signals received from the first and the second data latch, and that outputs said data signals stored in the sampling memory to a digital to analog converter (Fig. 5, ch. 17) for converting said stored data signals to an analog signal (paragraph 115).

As to claim 13, Fujino discloses a data driver IC for a liquid crystal display device, characterized in that the data driver IC inputs a first clock signal (paragraphs 71 and 74) and a second clock signal in reverse relation with the first clock signal and a selection signal (paragraphs 71, 74, and 116), selects the first or second clock signal based on the selection signal, and can selectively latch data signals with the first or second clock signal (paragraphs 71, 74, and 116).

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance: Claims 1-6 are allowable since certain key features of the claimed invention are not taught or fairly suggested by the prior art. In claim 1, "load means for making the load capacitance of the second clock signal line equal to or substantially equal to the load capacitance of the first clock signal line". The closest prior art of record Fujino et al. (US 2003/0043126) however singularly or in combination fails to anticipate or render the above underlined limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Applicant's amendment, adding new claim 13, necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to RICARDO L. OSORIO whose telephone number is (571) 272-7676. The examiner can normally be reached on MONDAY-THURSDAY 7:00 am-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, AMARE MENGISTU can be reached on (571) 272-7674. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/RICARDO L OSORIO/
Primary Examiner, Art Unit 2629